

## **FACULTY OF ENGINEERING AND BUILT ENVIRONMENT** SEMESTER FINAL EXAMINATION SCHEDULE

DEPARTMENT:	ELECTRICAL AND ELECTRONICS ENGINEERING			
PROGRAM NAME:	MASTER OF SCIENCE IN ELECTRICAL AND ELECTRONICS ENGINEERING			
INTAKE:	2407 & 2411 (2 Students)			
SEMESTER:	JAN 2025			
ACADEMIC SESSION:	6 JAN to 31 MAY	*		

Date/Day	TIME	SUBJECT CODE	SUBJECT NAME	ROOM NO
23.06.2025	00 20 414 12 20 014	MEE 1123	ADVANCED VLSI DESIGN	Exam Hall Level 3
MONDAY	09.30 AM -12.30 PM	IVIEE 1123	ADVANCED VESI DESIGN	LXAIII HAII LEVEI 5
24.06.2025	00 20 AM 12 20 DM	MEE 1213	ELECTRIC POWER DISTRIBUTION SYSTEM	Exam Hall Level 3
TUESDAY	09.30 AM -12.30 PM	MIEE 1213	ELECTRIC POWER DISTRIBUTION STSTEM	LAGIII I I GII LEVEI 3
25.06.2025	09.30 AM -12.30 PM	MEE 1223	POWER ELECTRONICS	Exam Hall Level 3
WEDNESDAY	09.30 AIVI -12.30 PIVI	IVIEE 1223	POWER ELECTRONICS	LXaiii Haii Level 3
26,06.2025	00 30 AM 43 30 BM	MET 1233	IMAGE PROCESSING	Exam Hall Level 3
THURSDAY	09.30 AM -12.30 PM	IVIE ( 1233	IMAGE PROCESSING	Examinan Level 3
30.06.2025	00 20 414 42 20 DM	MET 1202	PROJECT MANAGEMENT	Exam Hall Level 3
MONDAY	09.30 AM -12.30 PM	MET 1283	PROJECT WANAGEMENT	LAGIII Hall Level 5

PREPARED BY,

NAME: MS. SUPRAJA SRINIVASAN

DESIGNATION: PROGRAMME COORDINATOR

APPROVED BY,

NAME: DATO' IR. JAMALUDIN BIN NON

DESIGNATION: DEAN

VERIFIED BY,

NAMEE MIS. SARMELA
DESIGNATION: SENIOR ASST. REGISTRAR

ANAJAYA LEXAMINATION