



FACULTY OF ENGINEERING AND BUILT ENVIRONMENT

SEMESTER FINAL EXAMINATION SCHEDULE

DEPARTMENT:	ELECTRICAL AND ELECTRONICS ENGINEERING
PROGRAM NAME:	MASTER OF SCIENCE IN ELECTRICAL AND ELECTRONICS ENGINEERING
INTAKE:	2407 & 2411 (2 Students)
SEMESTER:	JAN 2025
ACADEMIC SESSION:	6 JAN to 31 MAY

Date/Day	TIME	SUBJECT CODE	SUBJECT NAME	ROOM NO
23.06.2025 MONDAY	09.30 AM -12.30 PM	MEE 1123	ADVANCED VLSI DESIGN	Exam Hall Level 3
24.06.2025 TUESDAY	09.30 AM -12.30 PM	MEE 1213	ELECTRIC POWER DISTRIBUTION SYSTEM	Exam Hall Level 3
25.06.2025 WEDNESDAY	09.30 AM -12.30 PM	MEE 1223	POWER ELECTRONICS	Exam Hall Level 3
26.06.2025 THURSDAY	09.30 AM -12.30 PM	MET 1233	IMAGE PROCESSING	Exam Hall Level 3
30.06.2025 MONDAY	09.30 AM -12.30 PM	MET 1283	PROJECT MANAGEMENT	Exam Hall Level 3

<p>PREPARED BY,</p> <p><i>Supraja</i></p> <p>NAME: MS. SUPRAJA SRINIVASAN DESIGNATION: PROGRAMME COORDINATOR</p>	<p>APPROVED BY,</p> <p><i>Jamaludin</i></p> <p>NAME: DATO' IR. JAMALUDIN BIN NON DESIGNATION: DEAN</p>	<p>VERIFIED BY,</p> <p><i>Sarmela</i></p> <p>NAME: MS. SARMELA DESIGNATION: SENIOR ASST. REGISTRAR</p> <p>ELANAJAYA LEARNING SITE EXAMINATION UNIT</p>
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