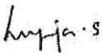






SCHOOL OF ENGINEERING AND BUILT ENVIRONMENT
SEMESTER FINAL EXAMINATION SCHEDULE

DEPARTMENT:	ELECTRICAL AND ELECTRONICS ENGINEERING
PROGRAM NAME:	MASTER OF SCIENCE IN ELECTRICAL AND ELECTRONICS ENGINEERING
INTAKE:	2403
SEMESTER:	JUNE 2025
ACADEMIC SESSION:	9 JUNE 2025- 03 OCT 2025

Date/Day	TIME	SUBJECT CODE	SUBJECT NAME	ROOM NO
25.09.2025 THURSDAY	09.00 AM -12.00 PM	MEE 1123	ADVANCED VLSI DESIGN	Exam Hall Level 3
26.09.2025 FRIDAY	09.00 AM -12.00 PM	MEE 1213	ELECTRIC POWER DISTRIBUTION SYSTEM	Exam Hall Level 3
29.09.2025 MONDAY	09.00 AM -12.00 PM	MEE 1223	POWER ELECTRONICS	Exam Hall Level 3
30.09.2025 TUESDAY	09.00 AM -12.00 PM	MET 1233	IMAGE PROCESSING	Exam Hall Level 3
01.10.2025 WEDNESDAY	09.00 AM -12.00 PM	MET 1283	PROJECT MANAGEMENT	Exam Hall Level 3

PREPARED BY,  <hr/> NAME: MS. SUPRAJA SRINIVASAN DESIGNATION: PROGRAMME COORDINATOR	APPROVED BY,  <hr/> NAME: DATO' IR. JAMALUDIN BIN NON DESIGNATION: DEAN	VERIFIED BY,  <hr/> NAME: MS. SARMELA DESIGNATION: SENIOR ASST. REGISTRAR EXAMINATION
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